

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore®
 RELEASE 1.6

 Welcome
 United States Patent and Trademark Office


» Se.

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)

Quick Links

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Your search matched **10** of **1027552** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or enter a new one in the text box.

graph<and>cut<and>(depth or buffer)

Search

☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 Performance and interface buffer size driven behavioral partitioning embedded systems***Ta-Cheng Lin; Sait, S.M.; Cyre, W.R.;*

Rapid System Prototyping, 1998. Proceedings. 1998 Ninth International Workshop on , 3-5 June 1998

Pages:116 - 121

[\[Abstract\]](#)[\[PDF Full-Text \(36 KB\)\]](#)**IEEE CNF****2 An in-depth study of graph partitioning measures for perceptual organization***Soundararajan, P.; Sarkar, S.;*

Pattern Analysis and Machine Intelligence, IEEE Transactions on , Volume: 25 , Issue: 6 , June 2003

Pages:642 - 660

[\[Abstract\]](#)[\[PDF Full-Text \(2943 KB\)\]](#)**IEEE JNL****3 Travelling wave electrode optimisation for high speed electro-optic modulators using the Fourier series method***Boyu, W.; Guangjun, X.; Xiaomin, J.;*

Optoelectronics, IEE Proceedings- , Volume: 141 , Issue: 6 , Dec. 1994

Pages:381 - 390

[\[Abstract\]](#)[\[PDF Full-Text \(528 KB\)\]](#)**IEEE JNL****4 Stereo matching with reflections and translucency***Yanghai Tsin; Sing Bing Kang; Szeliski, R.;*

Computer Vision and Pattern Recognition, 2003. Proceedings. 2003 IEEE Conference on , Volume: 1 , 18-20 June 2003

Pages:I-702 - I-709 vol.1

[[Abstract](#)] [[PDF Full-Text \(825 KB\)](#)] IEEE CNF

5 Combination of structural and state analysis for partial scan

Sharma, S.; Hsiao, M.S.;

VLSI Design, 2001. Fourteenth International Conference on , 3-7 Jan. 2001

Pages:134 - 139

[[Abstract](#)] [[PDF Full-Text \(464 KB\)](#)] IEEE CNF

6 A flow control mechanism to avoid message deadlock in k-ary n-cul networks

Carrion, C.; Beivide, R.; Gregorio, J.A.; Vallejo, F.;

High Performance Computing, 1997. Proceedings. Fourth International Conference on , 18-21 Dec. 1997

Pages:322 - 329

[[Abstract](#)] [[PDF Full-Text \(716 KB\)](#)] IEEE CNF

7 Design parameters of Ti:LiNbO₃ waveguides for optimum acoustooptical interaction efficiency

Gregoris, D.; Woolven, S.; Ristic, V.M.;

Lightwave Technology, Journal of , Volume: 8 , Issue: 2 , Feb. 1990

Pages:168 - 172

[[Abstract](#)] [[PDF Full-Text \(472 KB\)](#)] IEEE JNL

8 Joint 3D-reconstruction and background separation in multiple view using graph cuts

Goldlucke, B.; Magnor, M.A.;

Computer Vision and Pattern Recognition, 2003. Proceedings. 2003 IEEE Computer Society Conference on , Volume: 1 , 18-20 June 2003

Pages:I-683 - I-688 vol.1

[[Abstract](#)] [[PDF Full-Text \(399 KB\)](#)] IEEE CNF

9 Parallelized search for the optimal/sub-optimal solutions of task scheduling problem taking account of communication overhead

Kai, M.; Hatori, T.;

Communications, Computers and signal Processing, 2001. PACRIM. 2001 IEEE Pacific Rim Conference on , Volume: 1 , 26-28 Aug. 2001

Pages:327 - 330 vol.1

[[Abstract](#)] [[PDF Full-Text \(528 KB\)](#)] IEEE CNF

10 Optimized block based disparity estimation in stereo systems using maximum-flow approach

Thomo, I.; Malasiotis, S.; Strintzis, M.G.;

Computer Graphics, Image Processing, and Vision, 1998. Proceedings. SIBGR '98. International Symposium on , 20-23 Oct. 1998

Pages:410 - 417



[\[Abstract\]](#) [\[PDF Full-Text \(208 KB\)\]](#) **IEEE CNF**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) |
[New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore®
 RELEASE 1.6

 Welcome
 United States Patent and Trademark Office


>> Search

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Your search matched **11** of **1027552** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or enter a new one in the text box.

☐ Check to search within this result set
Results Key:
JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 **Graph cut: application to Bayesian emission tomography reconstruction**

Bonneville, M.; Meunier, J.; Roy, S.;

Image Analysis and Processing, 1999. Proceedings. International Conference on , 27-29 Sept. 1999

Pages:1184 - 1189

[\[Abstract\]](#)
[\[PDF Full-Text \(156 KB\)\]](#)

IEEE CNF

2 **Exact optimization for Markov random fields with convex priors**

Ishikawa, H.;

Pattern Analysis and Machine Intelligence, IEEE Transactions on , Volume: 25 , Issue: 10 , Oct. 2003

Pages:1333 - 1336

[\[Abstract\]](#)
[\[PDF Full-Text \(563 KB\)\]](#)

IEEE JNL

3 **Watermarking graph partitioning solutions**

Wolfe, G.; Wong, J.L.; Potkonjak, M.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction on , Volume: 21 , Issue: 10 , Oct. 2002

Pages:1196 - 1204

[\[Abstract\]](#)
[\[PDF Full-Text \(286 KB\)\]](#)

IEEE JNL

4 **Structural and navigational analysis of hypermedia courseware**

Chuen-Tsai Sun; Chien Chou; Bing-Kuen Lin;

Education, IEEE Transactions on , Volume: 41 , Issue: 4 , Nov. 1998

Pages:11 pp.

[\[Abstract\]](#)
[\[PDF Full-Text \(8 KB\)\]](#)

IEEE JNL

5 Criterion for reducing power consumption by means of ratio-cut circuit partitioning

Huazhong Yang; Guanzhang Hu; Hui Wang;

Electronics Letters , Volume: 36 , Issue: 16 , 3 Aug. 2000

Pages:1346 - 1347

[\[Abstract\]](#) [\[PDF Full-Text \(228 KB\)\]](#) IEEE JNL

6 Joint 3D-reconstruction and background separation in multiple view using graph cuts

Goldlucke, B.; Magnor, M.A.;

Computer Vision and Pattern Recognition, 2003. Proceedings. 2003 IEEE Computer Society Conference on , Volume: 1 , 18-20 June 2003

Pages:I-683 - I-688 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(399 KB\)\]](#) IEEE CNF

7 Learning Bayesian networks. II. A computational algorithm

Heping Pan;

Information Fusion, 2002. Proceedings of the Fifth International Conference on , Volume: 2 , 8-11 July 2002

Pages:777 - 783 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(476 KB\)\]](#) IEEE CNF

8 Combination of structural and state analysis for partial scan

Sharma, S.; Hsiao, M.S.;

VLSI Design, 2001. Fourteenth International Conference on , 3-7 Jan. 2001

Pages:134 - 139

[\[Abstract\]](#) [\[PDF Full-Text \(464 KB\)\]](#) IEEE CNF

9 Global optimal surface from stereo

Zhao, H.;

Pattern Recognition, 2000. Proceedings. 15th International Conference on , Volume: 1 , 3-7 Sept. 2000

Pages:101 - 104 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(500 KB\)\]](#) IEEE CNF

10 A routability and performance driven technology mapping algorithm: LUT based FPGA designs

Chi-Chou Kao; Yen-Tai Lai;

Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on , Volume: 1 , 30 May-2 June 1999

Pages:474 - 477 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(372 KB\)\]](#) IEEE CNF

11 Applications of global flow analysis in logic synthesis

Berman, L.; Trevillyan, L.; Brand, D.;

Circuits and Systems, 1988., IEEE International Symposium on , 7-9 June 19

Pages:901 - 904 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(264 KB\)\]](#) **IEEE CNF**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) |
[New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved